

ABSTRACT

A sensor signal conditioning circuit and sensor system incorporating
5 the same. In one embodiment, the signal conditioning circuit includes a DC-
coupled detector that converts a sensor signal into a discrete level signal.
An AC-coupled detector having a dynamic DC threshold input also converts
the sensor signal into a discrete level signal and has a startup delay
associated with the dynamic DC threshold input. The signal conditioning
10 circuit further includes a device that inhibits the DC-coupled detector
responsive to the dynamic DC threshold input reaching a specified threshold
voltage level such that the AC-coupled detector provides the detected output
during steady-state sensor operation.